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I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date: February 9, 2004

Sonia V. McVean
Sonia V. McVean

**RESPONSE UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE
GROUP ART UNIT 1756**

**PATENT
36856.834**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Koji YOSHIDA et al.	Art Unit: 1756
Serial No.: 10/035,400	
Filed: October 26, 2001	Examiner: J. Ruggles
Title: PHOTOLITHOGRAPHIC METHOD OF PRODUCING A THIN FILM CIRCUIT BOARD USED AS A MILLIWAVE OR MICROWAVE MODULE	

REQUEST FOR RECONSIDERATION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated October 9, 2003, please reconsider the above identified application in view of the following remarks.

A listing of the claims begins on page 2 of this paper for the convenience of the Examiner. No amendments are being made by this listing.

Remarks/Arguments begin on page 4 of this paper.